

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

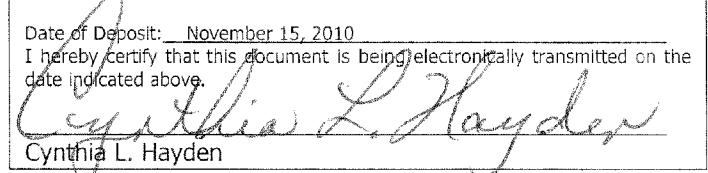
In re Applicant:	§	Art Unit:	2621
Louis Lippincott et al.			
Serial No.:	§	Examiner:	Chikaodili E. Anyikire
10/750,075	§	Conf. No.:	2083
Filed:	§	Docket:	ITL.1703US
December 31, 2003	§	P17498	
For:	§	Assignee:	Intel Corporation
Motion Estimation Sum of all Differences (SAD) Array Having Reduced Semiconductor Die Area Consumption	§		

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed September 1, 2010, please amend the above-referenced patent application as follows:

Date of Deposit: November 15, 2010
I hereby certify that this document is being electronically transmitted on the date indicated above.

Cynthia L. Hayden